

Appln. No.: 09/740,364  
Amendment Dated July 23, 2004  
Reply to Office Action of April 23, 2004

MATP-600US

**Amendments to the Specification:**

Please add the following new header and paragraph after the title:

**CROSS-REFERENCE TO RELATED APPLICATIONS**

This application is a continuation-in-part of U.S. Application No. 09/541,120, filed March 31, 2000, the contents of which are incorporated herein by reference.

Please amend the paragraph beginning at page 6, line 1 as follows:

Figure 1 is a block diagram of a television receiver system that includes an exemplary embodiment of the invention. The television receiver includes a tuner 106 that is coupled to receive radio frequency input signals from an antenna 100 and/or a cable connection 102. The system also includes a satellite receiver that receives television signals, via a dish antenna 104, that are encoded according to the standard specified by the Moving Picture Experts Group (MPEG). Both the tuner 106 and the satellite receiver 108 are controlled by a processor 110. The exemplary tuner 106 may receive analog television signals or signals encoded according to the standard specified by the Advanced Television Systems Committee (ATSC). This standard is a subset of the MPEG standard. The tuner provides analog television signals to an analog television signal processor 112 and provides ATSC encoded television signals to an MPEG decoder 114. Other MPEG encoded signals provided, for example, by the satellite receiver 108 are also provided to the MPEG decoder 114. Both the analog television signal processor 112 and the MPEG decoder 114 provide analog video and audio output signals. The video output signals are applied to a video signal processor 116 while the audio signals are applied to an audio signal processor 118. The video signal processor 116 generates video images for display on a display device 120 while the audio signal processor 118 generates accompanying sounds through a speaker system 122.

Please amend the paragraph beginning at page 8, line 25 as follows:

Channel matrix 20 comprises  $n$  columns and  $m$  rows for displaying  $n \times m$  channels numbers. In Figure 3, a  $10 \times 10$  matrix is shown for illustrative purposes. The

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channels are divided into groupings based on information contained in the channel matrix group indicator 22A. The channel matrix group indicator 22A, and selectors 22B and 22C, shown in Figures 3-9, form a group indicator and selector bar 22. Because the exemplary embodiment of the invention displays up to 100 channels at a time in the matrix 20, the value in group indicator 22A indicates a base channel number corresponding to the 00 entry of the matrix. Thus, the channel matrix shown in Figure 3 displays the status of the channel property identified by selector 15A for channel numbers 300 through 399.